Amendments to the CLAIMS:

Without prejudice, this listing of the claims replaces all prior versions and listings of the claims in the present application:

Listing of Claims:

1-17. (Canceled).

18. (Currently Amended) A motor vehicle control unit, comprising:

a processor;

a first interface for communicating with a functional unit of a motor vehicle; [[and]]

at least one second interface combined with the processor in a sub-assembly so as to minimize a capacitive loading by the at least one second interface; and

a storage module, wherein the at least one second interface accesses a code of the processor in the storage module for a writing purpose.

19. (Previously Presented) The motor vehicle control unit as recited in Claim 18, further comprising:

an engine control unit.

20. (Currently Amended) The motor vehicle control unit as recited in Claim 18, further comprising:

a storage module, wherein:

the at least one second interface accesses the storage module without participation of the processor.

21. (Canceled).

22. (Previously Presented) The motor vehicle control unit as recited in Claim 18, wherein:

the at least one second interface performs a block transfer of data.

23. (Previously Presented) The motor vehicle control unit as recited in Claim 18, wherein:

the first interface is combined with the processor and the at least one second interface in the sub-assembly.

24. (Previously Presented)   The motor vehicle control unit as recited in Claim 18, wherein:

      the sub-assembly includes a printed-circuit board.

25. (Previously Presented) The motor vehicle control unit as recited in Claim 18, wherein:

      the sub-assembly includes a semiconductor chip.

26. (Currently Amended) The motor vehicle control unit as recited in Claim 18, ~~further comprising:~~ wherein

      [[a]] the storage module for storing operating parameters of the processor, ~~wherein:~~

          ~~the storage module~~ is able to be at least one of written on and read out via the

at least one second interface.

27. (Previously Presented) The motor vehicle control unit as recited in Claim 18, wherein:

      the at least one second interface includes a serial interface.

28. (Previously Presented) The motor vehicle control unit as recited in Claim 18, wherein:

      the at least one second interface includes one of an ethernet and a FireWire interface.

29. (Previously Presented) The motor vehicle control unit as recited in Claim 18, wherein:

      the at least one second interface includes a USB interface.

30. (Previously Presented) The motor vehicle control unit as recited in Claim 18, wherein:

      the at least one second interface transmits data received from the processor via the

first interface in an isochronous mode.

31. (Previously Presented) The motor vehicle control unit as recited in Claim 18, wherein:

      the at least one second interface transmits control parameters of the processor in bulk

mode.

32. (Currently Amended) The motor vehicle control unit as recited in Claim 18, ~~further comprising:~~

      ~~a storage module,~~ wherein:

the at least one second interface is able to at least one of read and write to individual storage locations of the storage module in an interrupt mode.

33. (Previously Presented) The motor vehicle control unit as recited in Claim 18, wherein:

the at least one second interface is connected to no functional unit of a motor vehicle that is to be controlled.

34. (Currently Amended) A method for communicating between a motor vehicle control unit and an external host, comprising:

providing a first interface for communicating with a functional unit of the motor vehicle;

providing at least one second interface for communicating with the external host, the at least one second interface being combined with a processor in a sub-assembly so as to minimize a capacitive loading by the at least one second interface;

providing a storage module, wherein the at least one second interface accesses a code of the processor in the storage module for a writing purpose; and

causing the external host to stipulate different USB endpoints and transmission modes for different types of data to be exchanged between the external host and the motor vehicle control unit.

35. (Previously Presented) The method as recited in Claim 34, further comprising:

causing the external host to poll the USB endpoints according to a priority sequence.

36. (Previously Presented) The motor vehicle control unit as recited in Claim 18, wherein the processor, the first interface and the at least one second interface are integrated on a shared semiconductor substrate.

37. (Previously Presented) The motor vehicle control unit as recited in Claim 36, wherein the entire motor vehicle control unit is integrated on a semiconductor substrate.